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| Notice of References Cited | Application/Control No. 10/689,592 | Applicant(s)/Patent Under Reexamination TANAKA, GENICHI | |
| | Examiner Chun-Kuan (Mike) Lee | Art Unit 2181 | Page 1 of 1 |

U.S. PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

| * | | Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages) |
|---|---|---|
| | U | "Introduction to ASIC Design Methodology", 1999 |
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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